

Single Phase Rectifier Bridge

V_{RRM} 200 to 1200V

I_D 50 Amp

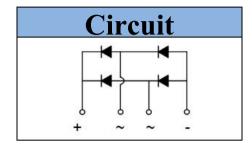
Features

- Glass passivated chip
- Ideal for printed circuit boards
- High surge current capability
- High temperature soldering guaranteed:265 °C/10 seconds

Applications

- Single phase rectifiers for power supplies
- Rectifiers for DC motor field supplies
- Industrial automation equipment
- Input rectifiers for inverter





Module Type

Type	V_{RRM}	V_{RSM}
GBJ5002	200V	300V
GBJ5004	400V	500V
GBJ5006	600V	700V
GBJ5008	800V	900V
GBJ5010	1000V	1100V
GBJ5012	1200V	1300V

Maximum Ratings

Symbol	Item	Conditions	Values	Unit
I _D	Output Current	Single Phase,Sin Full Wave T _c = 95°C	50	А
I _{FSM}	Surge Forward Current	$T_j = 25$ °C, $t = 50$ Hz(10ms), $V_R = 0$ V	500	Α
l ² t	Circuit Fusing Consideration	t = 10ms T _j =25°C	1250	A ² s
V _{ISO}	Isolation Breakdown Voltage	AC 50Hz/60Hz; R.M.S; 1min	2500	V
Tj	Operating Junction Temperature		-40 to +150	°C
T _{stg}	Storage Temperature		-40 to +150	°C
Ms	Mounting Torque	(Recommended torque:0.65 N·m)	0.8	N⋅m
Weight	Module (Approximately)		7	g

Thermal Characteristics

Symbol	Item	Conditions	Values	Unit
R _{th(j-c)}	Thermal Impedance, Max	Junction to Case(Per Total)	0.7	°C/W
		Junction to Case(Per Diode)	2.8	°C/W

■ Electrical Characteristics

Symbol	Item	Conditions	Values			Unit
Symbol			Min.	Тур.	Max.	Ullit
$V_{\sf FM}$	Forward Voltage Drop, Max	$T_j = 25^{\circ}C$ $I_F = 25A$	_	_	1.1	V
I _{RRM} Repetitive Peak Reverse	Panatitiva Paak Payarsa Current May	$T_j = 25$ °C $V_R = V_{RRM}$	_	_	5	uA
	Repetitive Feak Reverse Current, Max	$T_j = 150$ °C $V_R = V_{RRM}$	_	_	0.5	mA
V _{T0}	Threshold Voltage, for power loss calculation only	T _j = 125°C	0.75		V	
r _T	Slope Resistance, for power loss calculation only	T _j = 125°C	3.9		mΩ	

Revised: 2024-06, Revision2.1



Performance Curves

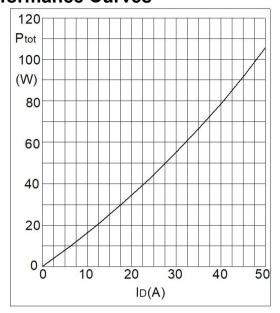


Fig1. Power Dissipation

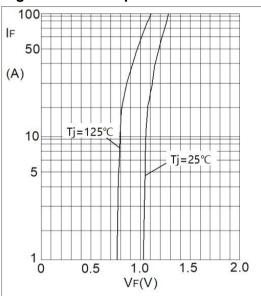


Fig3. Forward Characteristics

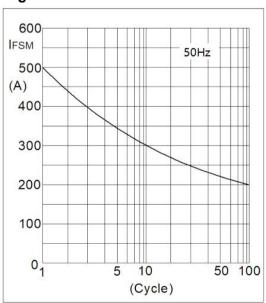


Fig5. Max Non-Repetitive Forward Surge Current

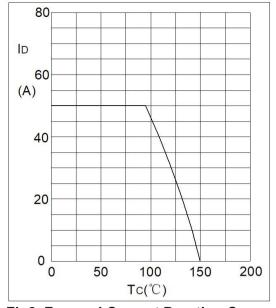


Fig2. Forward Current Derating Curve

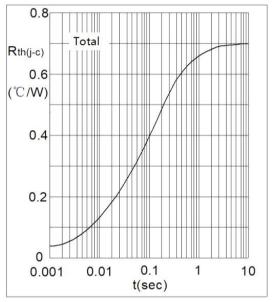
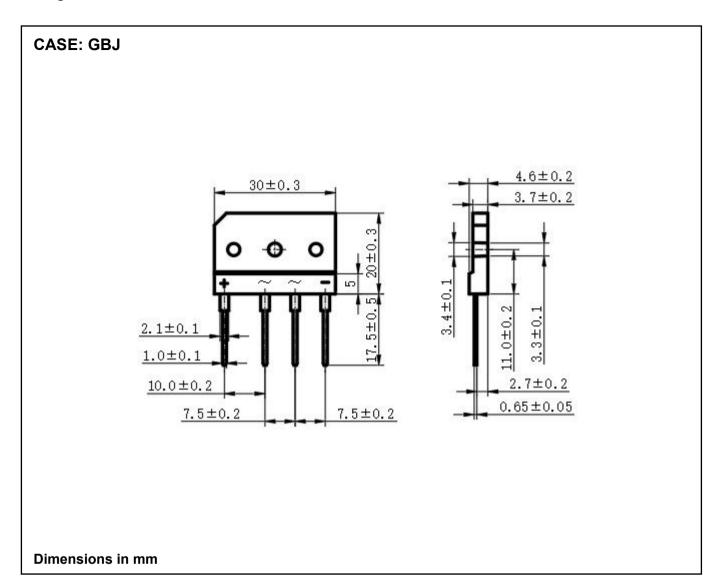


Fig4. Transient Thermal impedance



Package Outline Information



Revised: 2024-06, Revision2.1



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